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# KPTI A MITIGATION METHOD AGAINST MELTDOWN

4th WAMOS 18

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# OUTLINE

1. Introduction
2. Background
3. KASLR
4. KAISER
5. KPTI
6. Conclusion

# INTRODUCTION



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Meltdown: new attack, January 2018



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→ original concept

→ prevent side-channel attacks against KASLR



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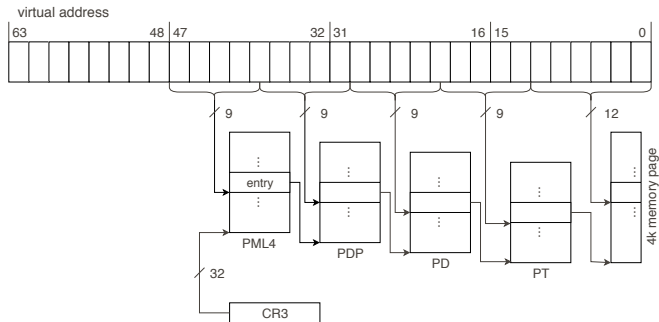
→ prevent side-channel attacks against KASLR

KASLR: Kernel Address Space Layout Randomization

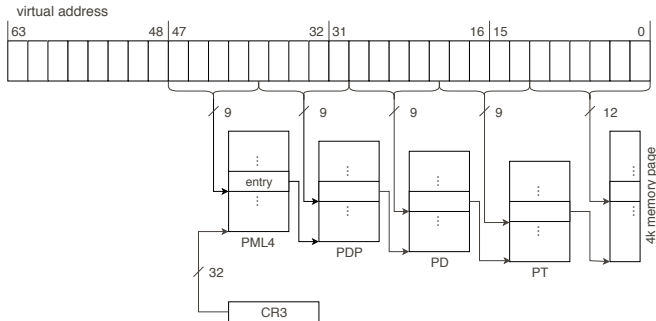
BACKGROUND



# VIRTUAL ADDRESS SPACE



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Context switch: switch between two processes  
 → CR3 update

# VIRTUAL ADDRESS SPACE

User - kernel space switch:

Kernel space is mapped into every user process

→ No CR3 update

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TLB: Translation Lookaside Buffer

→ Cache for Page Table Entries

→ Performance increase

# MELTDOWN

Out-of-Order Execution:

→ Improves performance

Approach:

1. Inaccessible kernel memory is loaded → exception
2. Out-of-order execution of following code
3. Content of accessed kernel memory is leaked through cache side-channel

→ Entire physical memory can be read

KASLR

# KASLR

## Kernel Address Space Layout Randomization

Randomize placement of kernel at boot time  
→ To secure the kernel address information

Attacks:

Double Page Fault Attack,  
Intel TSX-based Attack,  
Prefetch Side-Channel Attack

# DOUBLE PAGE FAULT ATTACK

Allocated: page belongs to the address space

Accessible: right access privilege

1. Access inaccessible kernel memory
2. First page fault
  - 2.1 Page allocated → cached
  - 2.2 Page not allocated → not cached
3. Second page fault
  - 3.1 Cached → less time
  - 3.2 Not cached → more time
4. Learning if kernel memory is allocated



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→ KASLR is dead.

KAISER

## KAISER

## Kernel Address Isolation to have Side channels Efficiently Removed

Published in July 2017

Prevent side-channel attacks against KASLR

→ Isolate user address and kernel address space

# KAISER

## Kernel Address Isolation to have Side channels Efficiently Removed

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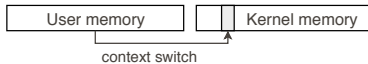
Prevent side-channel attacks against KASLR

→ Isolate user address and kernel address space

Before Meltdown:

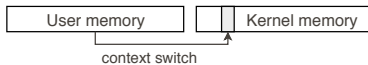
- Kernel space mapped in user space
- Protected through permission bits in translation tables

# DIFFERENT MODELS

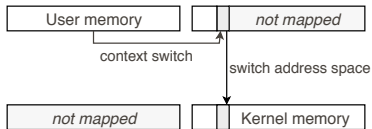


(a) Regular OS

# DIFFERENT MODELS

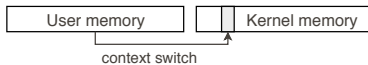


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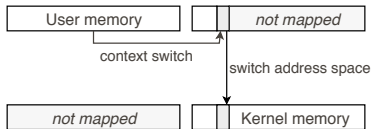


(b) Stronger Kernel Isolation

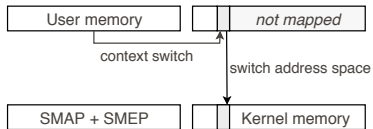
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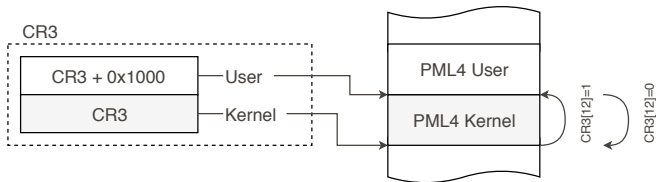


(b) Stronger Kernel Isolation



(c) KAISER

# PARTITIONING





# CHALLENGES

1. Minimizing the Kernel Address Space Mapping  
for context switch some locations need to be mapped

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more address space switches ⇒ more TLB flushes

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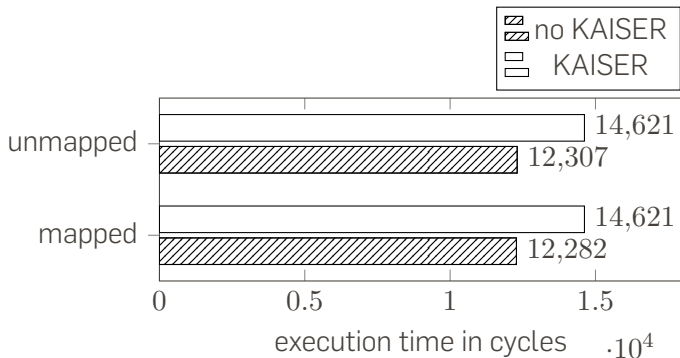
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→ Interrupts, exceptions, system calls

2. Efficient and Secure TLB Management  
more address space switches ⇒ more TLB flushes  
→ PCIDs

Process context identifiers  
→ Each TLB-entry marked with process id

# ATTACK HANDLING

## Double Page Fault Attack



KPTI

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## Kernel Page Table Isolation

Initial Linux patch by Dave Hansen

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- PCIDs
- Trampoline functions



# KPTI

## Kernel Page Table Isolation

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→ PCIDs

→ Trampoline functions

Current Status:

Linux: 4.15

Windows: 17035

MacOs: 10.13.2

# EVALUATION

Syscalls, interrupts and exceptions

→ Performance loss can vary heavily:  $\sim 5\%$   $\geq 30\%$

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	lseek
no kaiser:	5.2 M/s
kaiser+ pcid:	3.0 M/s
kaiser+nopcid:	2.2 M/s

CONCLUSION

# CONCLUSION

- Best short-term solution
- Performance loss varies heavily
- New hardware or microcode update?

# SOURCES



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# THE END

Thank you for your attention.